

GE-128433

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor: Application of: Robert G. Wodnicki

Group Art Unit:

Serial No.: 10/605,093

Examiner:

Filed: September 8, 2003

Title: METHOD AND APPARATUS FOR HIGH-VOLTAGE SWITCHING OF
ULTRASOUND TRANSDUCER ARRAY

Commissioner for Patents
P.O. Box 1450
Alexandra, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

Pursuant to 37 CFR § 1.56, Applicants request that the references cited in the accompanying Form PTO/SB/08A be considered by the examiner in the above-entitled application and made of record therein. A copy of the cited references is enclosed.

Respectfully submitted,

Dennis M. Flaherty
Registration No. 31,159

March 5, 2004
Date

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CERTIFICATE OF MAILING

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Dennis M. Flaherty

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PTO/SB/08A (10-96)

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet

1

of

2

Complete if Known

Application Number

10/605,093

Filing Date

September 8, 2003

First Named Inventor

Robert G. Wodnicki

Group Art Unit

Examiner Name

Attorney Docket Number

GE RD 128433

U.S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

[illegible]

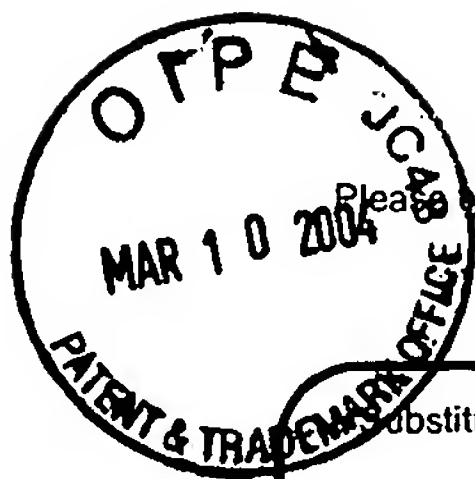
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Date
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* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Unique citation designation number. ² See attached Kinds of U.S. Patent Documents. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet 2 of 2

Complete if Known

Application Number	10/605,093
Filing Date	September 8, 2003
First Named Inventor	Robert G. Wodnicki
Group Art Unit	
Examiner Name	
Attorney Docket Number	GE RD 128433

OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	E	PROCEEDINGS OF POWERCON 7: SEVENTH NATIONAL SOLID-STATE POWER CONVERSION CONFERENCE, SAN DIEGO, CALIFORNIA, MARCH 24-27, 1980; NATIONAL SOLID-STATE POWER CONVERSION (7 TH); 1980; SAN DIEGO CALIF.. 3500 J.2.1-J.2.11; FRAGILE WITH USING THE POWER MOSFETS INTEGRAL REVERSE; VENTURA, CA: POWER CONCEPTS, 1980	
	F	A COPLANAR CMOS POWER SWITCH; ERNST HABEKOTTE, BERND HOFFLINGER, WOLFGANG RENKER, AND GUNTER ZIMMER, IEEE JOURNAL OF SOLID-STATE CIRCUITS, VOL. SC-16 NO.2, JUNE 1981	
	G	A VERSITILE MICROPOWER HIGH-VOLTAGE FLAT PANEL DISPLAY DRIVER IN A 100-V 0.7-UM CMOS INTELLIGENT INTERFACE TECHNOLOGY; JAN DOUTRELOIGNE, HERBERT DE SMET, AND ANDRE VAN CALASTER, IEEE JOURNAL OF SOLID-STATE CIRCUITS, VOL. 36, NO. 12, DECEMBER 2001	
	H	A MONOLITHIC 200-V CMOS ANALOG SWITCH; JAMES D. PLUMMER, AND JAMES D. MEINDL; IEEE JOURNAL OF SOLID-STATE CIRCUITS, VOL. SC-11, NO. 6, DECEMBER 1976	
	I	A HIGH VOLTAGE MOS SWITCH; KRISHNA C. SARASWAT, JAMES D. MEINDL, AND JOSEF BERGER; IEEE JOURNAL OF SOLID-STATE CIRCUITS, VOL. SC-10, NO. 3, JUNE 1975	

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